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15.32/5685ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 2023102/04/01  
09/30/99  
02/04/01  
JC90376528  
PTD

Dear Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): Tadashi KOMIYAMA, Akitoshi HARA, and Eiichi SATO

For: METHODS FOR MANUFACTURING SEMICONDUCTOR CHIPS, METHODS FOR  
MANUFACTURING SEMICONDUCTOR DEVICES, SEMICONDUCTOR CHIPS, SEMICONDUCTOR  
DEVICES, CONNECTION SUBSTRATES AND ELECTRONIC DEVICES

Enclosed are:

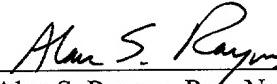
20 Pages of text and 6 Sheet(s) of drawings ( formal  informal) + 0 extra copies  
 A certified copy of 2000-28366, filed 4 Feb. 2000, in Japan, and 2001-14025, filed 23 Jan. 2001, in Japan,  
 from which priority is claimed in the subject case pursuant to Rule 55(b) and 35 USC 119.  Will follow.  
 Declaration and Power of Attorney.  Will Follow.

CALCULATION OF FEES					
ITEM		NUMBER OF CLAIMS FILED MINUS BASE*	NUMBER OF CLAIMS OVER BASE x RATE SM/LG ENTITY	\$ AMOUNT	\$ FEE
A	TOTAL CLAIMS FEE	23 - 20* =	x \$9.00 or 3 x \$18.00	\$54	
B	INDEPENDENT CLAIMS FEE**	7 - 3* =	x \$40.00 or 4 x \$80.00	\$320	
C	SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B)				\$374.00
D	BASIC FEE - SMALL ENTITY FEE = \$355.00 / LARGE ENTITY FEE = \$710				\$710.00
E	MULTIPLE-DEPENDENT CLAIMS FEE - SMALL ENTITY FEE = \$130.00 / LARGE ENTITY FEE = \$260				\$0
F	ASSIGNMENT RECORDING FEE				\$40.00
G	TOTAL FEE (ADD LINES C, D, E AND F)				\$1084.00

\*\*LIST INDEPENDENT CLAIMS 1, 4, 6, 9, 11, 17, 22

 A check in the amount of \$ 1084.00 to cover the filing fee is enclosed. A copy of this letter is enclosed. The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-0585. A duplicate copy of this sheet is enclosed. Any additional filing fees required under 37 CFR 1.16. Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

  
 Alan S. Raynes, Reg. No. 39,809
Date: February 4, 2001KONRAD RAYNES & VICTOR LLP  
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Customer No. 24033

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

JG903 U.S. PRO  
09/176528  
02/04/01



In re the Application of )  
KOMIYAMA et al. )

Serial No.: To Be Assigned )

Filing Date: )

For: METHODS FOR MANUFACTURING SEMICONDUCTOR )  
CHIPS, METHODS FOR MANUFACTURING )  
SEMICONDUCTOR DEVICES, SEMICONDUCTOR CHIPS, )  
SEMICONDUCTOR DEVICES, CONNECTION )  
SUBSTRATES AND ELECTRONIC DEVICES )

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Assistant Commissioner for Patents  
Washington, DC 20231

Express Mail Label No.: EL 273 653 750 US  
Date of Deposit: February 4, 2001

I hereby certify that a utility patent application and patent application fee transmittal; patent application including 20 pages of text (including an abstract and 23 claims), 6 sheets of drawings; a \$1084 check for the filing fee; and a return postcard are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

February 4, 2001  
Date of Deposit

Alan S. Raynes  
Name of person mailing papers

Alan S. Raynes  
Signature